

SIDDHARTH SETH

034, CENTER FOR INTEGRATED SYSTEMS,
420 VIA PALOU, STANFORD, CA - 94305.

650 714 9392 SIDSETH@STANFORD.EDU
HTTP://WWW.STANFORD.EDU/~SIDSETH/

EDUCATION:

- **Stanford University, California, USA** [September 2006 to present]
Ph.D. Student in Electrical Engineering (Advisor: Prof. Thomas H. Lee)
M.S. in Electrical Engineering (GPA: 3.99/4.0)
Robert Bosch Fellow (Stanford Graduate Fellowship)
- **Indian Institute of Technology, Kharagpur, India** [July 2002 to May 2006]
Bachelor of Technology (Honors) in Electronics & Electrical Communication Engineering (GPA: 9.67/10.0, Rank 1)
Dr. B. C. Roy Gold Medal for best all-round performance.

WORK / RESEARCH EXPERIENCE:

- **Broadcom Corporation, San Jose, CA – Summer Internship** [June 2008 to September 2008]
Design & Layout of Mixed Signal Circuits
- **Stanford University – Teaching Experience** [April 2008 to present]
Teaching Assistant for EE414 & EE344 - Design of Discrete RF Circuits for Communication Systems
- **Stanford University – Graduate Research Work** [September 2007 to present]
Adaptive RF Transceivers – Making bias power adaptive to system requirements (Stanford Microwave Integrated Circuits Lab)
- **National Semiconductor, Santa Clara, CA – Summer Internship** [June 2007 to September 2007]
Design, Layout & Testing of a SAR ADC (Data Conversion Systems Group)
- **IIT Kharagpur, India - B.Tech. Thesis Projects** [January 2005 to May 2006]
“Sampled Analog Architecture for 1-D DCT Switched Current Circuits” & “UWB Signal Generation & Applications”
- **Microsoft Research, Redmond, WA – Summer Internship** [May 2005 to July 2005]
MSR Sense: Spreadsheet Based Programming & Managing of Sensor Networks (Networked Embedded Computing Group)
- **IISc Bangalore, India – Summer Internship** [May 2004 to July 2004]
Performance Analysis of 802.11 mixed b/g networks

GRADUATE COURSEWORK EXPERIENCE (BY SUMMER 2008):

- **Academic Courses:** Analog Circuit Design (EE214), Bipolar Circuit Design (EE215), CMOS RF Circuit Design (EE314), VLSI Data Conversion Circuits (EE315), Wireless Communication (EE359), Digital VLSI Circuits & Systems (EE271 & EE313), Semiconductor Devices (EE216), Statistical Signal Processing (EE278), Linear Dynamical Systems (EE263)
- **Laboratory Courses:** High Frequency Laboratory – Made a Wideband Amplifier & VCO (360 to 620 MHz) using discrete components (EE344), Design of Discrete RF Circuits for Communication Systems - Design & Construction of every block in a 1-GHz transceiver using discrete components (EE414)
- **Term Projects:** Design of Fully Differential Telescopic Two Stage OTA, Design of a Class-A RF Power Amplifier, Design of a Differential Track & Hold Circuit, Debugging an IP Router & LPM Module Implementation, Design of 64 kb SRAM

TECHNICAL SKILLS:

- Circuit Design & Optimisation Software Packages: **Cadence Suite – Schematic & Layout Editor, Agilent ADS, MATLAB**
- Programming Languages: **HSPICE** (advanced), **Verilog HDL** (adequate), **C** (advanced), **C++ / C#** (adequate)

PUBLICATIONS:

- Siddharth Seth, Alec Woo, Tim Olson, Jie Liu & Feng Zhao, “A Spreadsheet Toolkit for Streaming Sensor Data,” Proceedings of the 3rd ACM Conference on Embedded Networked Sensor Systems (Sensys) 2005, Page: 313
- Alec Woo, Siddharth Seth, Tim Olson, Jie Liu & Feng Zhao, “A Spreadsheet Approach to Programming and Managing Sensor Networks,” Proceedings of the IEEE & ACM Fifth International Conference on Information Processing in Sensor Networks (IPSN) 2006, Page: 424-431

PATENT:

- Filed an application for a **Letters Patent** with the US Patent and Trademark Office, titled **Programming and Managing Sensor Networks** along with co-inventors Jie Liu, Alec Woo and Feng Zhao [July 2006]