

Daniel U. Becker

Personal Data

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Education

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|-------------------|--|---------------------|
| Since 09/2007 | Stanford University | Stanford, CA, USA |
| | Ph.D. Student, Electrical Engineering | |
| | <ul style="list-style-type: none">• Research focus on router microarchitecture for networks-on-chip (supervised by Prof. Dally)• Coursework in computer architecture, interconnection networks, reliability and digital design• Current grade point average: 4.0¹ | |
| 10/1999 – 06/2005 | RWTH Aachen | Aachen, Germany |
| | Dipl.-Ing., Electrical Engineering | |
| | <ul style="list-style-type: none">• Diploma thesis: "Modeling networks-on-chip using deterministic and stochastic Petri Nets" (supervised by Prof. Noll)• Coursework with focus on computer architecture, VLSI, embedded systems, operating systems and networks• Grade average: 1.1² with honors | |
| 09/2002 – 06/2003 | University of Washington | Seattle, WA, USA |
| | Visiting Graduate Student, Electrical Engineering | |
| | <ul style="list-style-type: none">• Coursework in stochastics, digital design, computer networks, digital communications, artificial intelligence and information theory• Grade point average: 3.9¹ | |
| 08/1989 – 06/1998 | Konrad-Adenauer-Gymnasium | Meckenheim, Germany |
| | Abitur | |
| | <ul style="list-style-type: none">• Major courses: Mathematics and physics• Cumulative grade average: 1.0² | |

Publications

1. D.U. Becker and W.J. Dally, Allocator Implementations for Network-on-Chip Routers. In *SC '09: Proceedings of the 2009 ACM/IEEE Conference on High Performance Computing, Networking, Storage and Analysis*, 2009.
2. H. Blume, T. von Sydow, D. Becker and T.G. Noll, Application of Deterministic and Stochastic Petri Nets for Performance Modeling of NoC Architectures, *Journal of Systems Architecture*, 53(8):466-476, 2007.
3. H. Blume, T. von Sydow, D. Becker and T.G. Noll, Modeling NoC Architectures by Means of Deterministic and Stochastic Petri Nets, In *SAMOS '05: Proceedings of the 5th International Workshop on Systems, Architecture, Modeling and Simulations*, pages 374-383, 2005.

¹ in American 4-point grade system (scale from 4 to 1, where 4 is the best possible mark)

² in German grade system (scale from 1 to 6, where 1 is the best possible mark)

Scholarships and Awards

- Stanford Graduate Fellowship (since 09/2007)
- RWTH Aachen “Springorum-Denkmünze” award 2006
- Infineon Student MemberChip Program (June 2004 – June 2005)
- Prof. Dr. Koepchen scholarship (winter semester 2003 / 2004 – summer semester 2005)
- Student scholarships for Apple Worldwide Developers Conferences (2003 / 2004 / 2005)
- Fulbright scholarship (academic year 2002 / 2003)

Work Experience

| | | |
|-------------------|--|---------------------|
| Since 10/2007 | CVA Group, Stanford University | Stanford, CA, USA |
| | Research Assistant | |
| | <ul style="list-style-type: none">• Working on router microarchitecture for networks-on-chip | |
| 08/2005 – 09/2007 | IBM Deutschland Entwicklung GmbH | Böblingen, Germany |
| | Engineer in development (microprocessor logic design) | |
| | <ul style="list-style-type: none">• Design, implement and test pervasive logic for server microprocessors• Two pending patent applications (20080209245, 20080222400) | |
| 05/2004 – 10/2004 | Siemens Corporate Research, Inc. | Redmond, WA, USA |
| | Intern (embedded software development) | |
| | <ul style="list-style-type: none">• Design and implement Windows CE device driver for GigaSet m34 USB | |
| 10/1998 – 08/2003 | dBm EDV-Handel und Support | Meckenheim, Germany |
| | Owner (self-employed) | |
| | <ul style="list-style-type: none">• Build, configure, test, sell and support PC systems• Accounting and tax preparation | |
| 08/1999 – 09/1999 | Moeller Holding GmbH | Bonn, Germany |
| | Intern (basic electrical practical training) | |
| 06/1999 – 07/1999 | Siemens AG, A&D Persa | Nuremberg, Germany |
| | Intern (basic mechanical practical training) | |
| 07/1998 – 04/1999 | Bundeswehr, LwWerft 84 / RechInst | Mechernich, Germany |
| | Basic military service conscript | |
| | <ul style="list-style-type: none">• Build, configure, test and repair PC systems and peripherals | |
| 06/1997 – 06/1998 | Stuch EDV GmbH | Meckenheim, Germany |
| | Student worker | |
| | <ul style="list-style-type: none">• Build, deploy and support PC-based cash registers for bakeries | |

Skills

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| Languages: | German (native), English (fluent), French (elementary) |
| Operating systems: | Mac OS X, Windows, UNIX |
| Programming languages: | C/C++, VHDL, Verilog, assembly, shell programming |
| Application software: | Matlab, LaTeX, FrameMaker |

References

Available upon request.