

# Angel Abusleme

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**Objectives** Intern position in the field of IC Design.

Education	Dates	Institution	Degree/Major
	2003 – Today	Stanford University	Ph.D. Electrical Engineering (not yet complete)
	1999 – 2000	Pontificia Universidad Católica de Chile	M.Sc. in Automatic Control (with highest honors)
	1993 – 2000	Pontificia Universidad Católica de Chile	Electrical Engineer (with highest honors)
	1993 – 1998	Pontificia Universidad Católica de Chile	B.Sc. in Electrical Engineering (with highest honors)

- Publications**
- ABUSLEME, A., CIPRIANO, A., GUARINI, M. *A Low Cost Altitude Control System for the Kadet Senior Radio-Controlled Airplane*. IEEE Transactions on Education, February 2003.
  - ABUSLEME, A., DIXON, J., SOTO, D. *Improved Performance of a Battery Powered Electric Car, Using Photovoltaic Cells*. IEEE Powertech 2003, June 23 - 26, Bolonia, Italy.
  - ABUSLEME, A., CIPRIANO, A. *Real-Time Longitudinal Control System for the Kadet Senior Radiocontrolled Airplane: A Low-Cost Testbed Design*. 2nd IFAC Conference on Mechatronic Systems, December 9 - 11, 2002, Berkeley, California.
  - ABUSLEME, A., CIPRIANO, A., GUARINI, M. *Fuzzy Control for the Kadet Senior Radiocontrolled Airplane*. ECC2001 European Control Conference. September 4-7, Porto, Portugal.
  - ABUSLEME, A., CIPRIANO, A. *Experiencia de Levitación Magnética de Bajo Costo para Laboratorio de Control Automático*. XIV Chilean Conference on Electrical Engineering. November 19-23, 2001, Antofagasta, Chile.
  - ABUSLEME, A., CIPRIANO, A., GUARINI, M. *Modeling and Simulation of the Radiocontrolled Airplane Kadet Senior*. XIV Chilean Conference on Automatic Control. October 23 - 27, 2000, Concepción, Chile.

## Research Experience

- Co-Researcher in Project Fondecyt 1020982 **Estudio y Aplicación de Nuevas Tecnologías para el uso eficiente de la Energía en Vehículos Eléctricos**, led by Professor Juan Dixon, Chile. Mar. 2002 – Jul. 2003.
- Development of M.Sc. Thesis Control **Difuso de Vehículo Volador no Tripulado**. Advisor: Professor Aldo Cipriano, Chile. Mar. 1999 – Nov. 2000.

## Working Experience

- Lecturer, Pontificia Universidad Católica de Chile (2001 – 2003)
- Design Engineer, Tecnología Integral SA (Intern, Summer, 1999)

## Teaching Experience

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| Courses Taught (Chile)            | <ul style="list-style-type: none"><li>• IEE2412 Electronics (II-2001 and II-2002)</li><li>• ING1003 Engineering Challenges (I-2002 and I-2003)</li><li>• IEE2900 Introduction to Microbotics (II-2002 and I-2003)</li><li>• IEE1122 Circuit Analysis (I-2003)</li><li>• IEE2612 Automatic Control (II-2001)</li></ul> |
| Courses Developed (Chile)         | <ul style="list-style-type: none"><li>• ING1003 Engineering Challenges</li><li>• IEE2900 Introduction to Microbotics</li></ul>  |
| Courses Assisted as TA (Stanford) | <ul style="list-style-type: none"><li>• EE14N Things About Stuff</li></ul>  |

## EE Courses taken at Stanford

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| <ul style="list-style-type: none"><li>• EE108A Digital Systems I</li><li>• EE108B Digital Systems II</li><li>• EE201A Seminar</li><li>• EE201B Seminar</li><li>• EE212 IC Fabrication Processes</li><li>• EE214 Analog Integrated Circuit Design</li><li>• EE215 Bipolar Analog Integrated Circuits</li><li>• EE216 Principles and Models of Semiconductor Devices</li><li>• EE263 Introduction to Linear Dynamical Systems</li><li>• EE271 Introduction to VLSI Systems</li></ul> | <ul style="list-style-type: none"><li>• EE310 Integrated Circuits Technology and Design Seminar</li><li>• EE313 Digital MOS Integrated Circuits</li><li>• EE314 RF Circuit Design</li><li>• EE315 VLSI Data Conversion Circuits</li><li>• EE392B Image Sensors</li><li>• EE392M Control Engineering in Industry</li><li>• EE410 IC Fabrication Laboratory</li><li>• EE414 RF Circuits for Communication Systems</li></ul> |
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**Awards**

- “Presidente de la República” Scholarship, to pursue a PhD degree in IC Design at Stanford University, 2003 – 2006.
- Best Graduated Engineer in year 2000. Colegio de Ingenieros de Chile, 2001.
- Roberto Ovalle Aguirre Award, for the best engineering thesis in year 2000. Instituto de Ingenieros de Chile, 2001.
- Shell Scholarship (1999) for graduate students.

**Computer Skills**

- Programming Languages: C, C++, Visual C++; Assembler for MIPS, Intel 8048 and PIC processors.
- HDLs: Verilog.
- Electronic Design Tools: HSPICE, LTSPICE, PSPICE, IRSIM, Sue, Magic, ModelSim.
- Engineering Tools: MATLAB, SimuLink, SciLab.
- Typesetting Software: LaTeX.
- Office Suites: Sun StarOffice, MS Office.
- Operating Systems: MS Windows, MS-DOS, Linux, Unix.

**Languages**

- Spanish: native speaker.
- English: fluent.